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(54) **Method for fabricating a thin film transistor having an amorphous oxide as a channel layer**

Herstellungsverfahren eines Dünnschichttransistors mit einem amorphen Oxid als Kanalschicht

Méthode de fabrication d'un transistor à couche mince avec un canal constitué par une couche d'oxyde amorphe

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Description

Technical Field

[0001] The present invention relates to a method of forming thin film transistors according to claim 1.

Background Art

[0002] A thin film transistor (TFT) is a three-terminal element having a gate terminal, a source terminal, and a drain terminal. It is an active element in which a semiconductor thin film deposited on a substrate is used as a channel layer for transportation of electrons or holes and a voltage is applied to the gate terminal to control the current flowing in the channel layer and switch the current between the source terminal and the drain terminal. Currently, the most widely used TFTs are metal-insulator-semiconductor field effect transistors (MIS-FETs) in which the channel layer is composed of a polysilicon or amorphous silicon film.

[0003] Recently, development of TFTs in which ZnO-based transparent conductive oxide polycrystalline thin films are used as the channel layers has been actively pursued (Patent Document 1). These thin films can be formed at low temperatures and is transparent in visible light; thus, flexible, transparent TFTs can be formed on substrates such as plastic boards and films.

[0004] However, known ZnO rarely forms a stable amorphous phase at room temperature and mostly exhibits polycrystalline phase; therefore, the electron mobility cannot be increased because of the diffusion at the interfaces of polycrystalline grains. Moreover, ZnO tends to contain oxygen defects and a large number of carrier electrons, and it is thus difficult to decrease the electrical conductivity. Therefore, it has been difficult to increase the on/off ratio of the transistors.

[0005] Patent Document 2 discloses an amorphous oxide represented by $Zn_xMyIn_zO_{(x+3y/2+3z/2)}$ (wherein M is at least one element selected from Al and Ga, the ratio x/y is in the range of 0.2 to 12, and the ratio z/y is in the range of 0.4 to 1.4). However, the electron carrier concentration of the amorphous oxide film obtained herein is $10^{18}/\text{cm}^3$ or more. Although this is sufficient for regular transparent electrodes, the film cannot be easily applied to a channel layer of a TFT. This is because it has been found that a TFT having a channel layer composed of this amorphous oxide film does not exhibit a sufficient on/off ratio and is thus unsuitable for TFT of a normally off type.

[0006] Minami, T. et al. (Journal of Vacuum Science and Technology A, pages 958-962, 18-10-1996) describes transparent conducting thin films made of Ga-doped polycrystalline In_2O_3 ($\text{In}_2\text{O}_3\cdot\text{Ga}$) with a maximum carrier concentration of $5 \times 10^{20}\text{cm}^{-3}$.

[0007] Phillips, J. M. et al. (Materials Research Society Symposium Proceedings, Vol. 345, pages 255-260, 05-04-1994) describes a transparent conductive poly-

crystalline GaInO_3 film made by pulsed laser deposition.

[0008] Yeom, H-Y. et al. (Thin Solid Films 411, pages 17-22, 22-05-2002) discloses d.c. magnetron-deposited tin-doped polycrystalline indium oxide (ITO).

[0009] Yaglioglu B. et al. (Mat.Res.Soc.Symp.Proc., Vol.747, pages V6.6.1-V6.6.6, 01-01-2003) discloses a structural study of an amorphous to crystalline transformation in In_2O_3 thin films.

[0010] Tadatsugu, M. et al. (Thin Solid Films 308-309, pages 13-18, 1997-10-31) reports on preparation of transparent conducting $\text{In}_4\text{Sn}_3\text{O}_{12}$ thin films by DC magnetron sputtering.

[0011] Narushima, S. et al. (Advanced Materials 2003, 15 No 17, September 3 pages 1409 - 1413) discloses amorphous InGaZn-O_4 as n-type semiconductor in p-n heterojunction diodes

[0012] Nomura, K. et al (Thin Solid Films 445 (2003) pages 322-326) discloses TFT using InGaO_3 (ZnO)_m (m=integer) single-crystalline thin films as n-channel layers.

Patent Document 1: Japanese Unexamined Patent Application Publication No. 2003-298062

Patent Document 2: Japanese Unexamined Patent Application

Publication No. 2000-044236

Disclosure of Invention

[0013] The present invention describes an amorphous oxide having a low electron carrier concentration and provides a thin film transistor having a channel layer composed of such an amorphous oxide.

[0014] It is described: (1) an amorphous oxide having an electron carrier concentration less than $10^{18}/\text{cm}^3$. In the present invention, the electron carrier concentration of the amorphous oxide is preferably $10^{17}/\text{cm}^3$ or less or $10^{16}/\text{cm}^3$ or less.

[0015] It is also described: (2) an amorphous oxide in which electron mobility thereof increases with the electron carrier concentration.

[0016] It is also described: (3) the amorphous oxide according to item (1) or (2) above, in which the electron mobility is more than $0.1 \text{ cm}^2/(\text{V}\cdot\text{sec})$.

[0017] It is also described: (4) the amorphous oxide according to item (2) or (3) above, exhibiting degenerate conduction. Note that "degenerate conduction" used herein is defined as a state in which the thermal activation energy for temperature dependency of electrical resistance is 30 meV or less.

[0018] It is also described: (5) the amorphous oxide according to any one of items (1) to (4) above, in which the amorphous oxide is a compound that contains at least one element selected from Zn, In, and Sn as a constituent and is represented by $[(\text{Sn}_{1-x}\text{M}_4)_2\text{O}_2]_a \cdot [(\text{In}_{1-y}\text{M}_3)_2\text{O}_3]_b \cdot [(\text{Zn}_{1-z}\text{M}_2)_2\text{O}]_c$ (wherein $0 \leq x \leq 1$, $0 \leq y \leq 1$, $0 \leq z \leq 1$; x, y, and z are not simultaneously 1; $0 \leq a \leq 1$, $0 \leq b \leq 1$, $0 \leq c \leq 1$, and $a + b + c = 1$; M4 is a group IV element (Si, Ge, or Zr) having an atomic number smaller than that of

Sn; M3 is Lu or a group III element (B, Al, Ga, or Y) having an atomic number smaller than that of In; and M2 is a group II element (Mg or Ca) having an atomic number smaller than that of Zn).

[0019] The amorphous oxide according (5) above may further contain at least one element selected from group V elements (V, Nb, and Ta) M5 and W.

[0020] One aspect of the present invention provides: (6) a method of forming a thin film transistor including the amorphous oxide according to claim 1.

[0021] It is also provided the amorphous oxide according to any one of (1) to (6) above formed on a glass substrate, a metal substrate, a plastic substrate, or a plastic film. It is also provided a field effect transistor including a channel layer composed of the amorphous oxide described above. The field effect transistor is **characterized in that** the gate insulating film is one of Al_2O_3 , Y_2O_3 , and HfO_2 or a mixed crystal compound containing at least two of these compounds.

[0022] It is also described: (7) a transparent semi-insulating amorphous oxide thin film comprising In-Ga-Zn-O, in which the composition in a crystallized state is represented by $\text{InGaO}_3(\text{ZnO})_m$ (wherein m is a number less than 6 and $0 < x \leq 1$), the electron mobility is more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ and the electron carrier concentration is less than $10^{18} / \text{cm}^3$.

[0023] It is also described: (8) a transparent semi-insulating amorphous oxide thin film comprising In-Ga-Zn-Mg-O, in which the composition in a crystallized state is represented by $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_m$ (wherein m is a number less than 6 and $0 < x \leq 1$), the electron mobility is more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ and the electron carrier concentration is less than $10^{18} / \text{cm}^3$. Moreover, the present invention also provides a method for forming the transparent semi-insulating amorphous oxide thin film in which an impurity ion for increasing the electrical resistance is not intentionally added and the deposition is conducted in an atmosphere containing oxygen gas.

[0024] A thin-film transistor according to another aspect of the present invention includes a source electrode, a drain electrode, a gate electrode a gate insulating film and a channel layer, in which the channel layer contains an amorphous oxide having an electron carrier concentration of less than $10^{18} / \text{cm}^3$. Preferably, the electron carrier concentration of the amorphous oxide is $10^{17} / \text{cm}^3$ or less or $10^{16} / \text{cm}^3$ or less. The amorphous oxide is an oxide as defined in claim 1.

[0025] In a thin film transistor of the present invention, a material in which the electron mobility increases with the electron carrier concentration can be used as the amorphous oxide.

[0026] According to the present invention, an amorphous oxide having a low electron carrier concentration can be provided, and a thin film transistor including a channel layer composed of such an amorphous oxide can be provided as defined in claim 1.

Best Mode for Carrying Out the Invention

[0027] An amorphous oxide of the present invention is **characterized in that** the electron carrier concentration is less than $10^{18} / \text{cm}^3$. A thin film transistor (TFT) of the present invention is **characterized in that** an amorphous oxide having an electron carrier concentration less than $10^{18} / \text{cm}^3$ is used in the channel layer.

[0028] For example, as shown in Fig. 5, the TFT is made by forming a channel layer 2 on a substrate 1 and a gate insulating film 3, a gate electrode 4, a source electrode 6, and a drain electrode 5 on the channel layer 2. In this invention, an amorphous oxide having an electron carrier concentration less than $10^{18} / \text{cm}^3$ is used in the channel layer.

[0029] The structure of the TFT to which the present invention can be applied is not limited to the staggered structure (top-gate structure) shown in Fig. 5 in which a gate insulating film and a gate terminal (electrode) are sequentially stacked on a semiconductor channel layer. For example, the TFT may have an inverted staggered structure (bottom-gate structure) in which a gate insulating film and a semiconductor channel layer are sequentially stacked on a gate terminal. The electron carrier concentration mentioned above is a value measured at room temperature. Room temperature is, for example, 25°C and, in particular, is appropriately selected from the range of about 0°C to about 40°C .

[0030] The electron carrier concentration of the amorphous oxide of the present invention need not be less than $10^{18} / \text{cm}^3$ all through the range of 0°C to 40°C . For example, it is sufficient if the carrier electron concentration is less than $10^{18} / \text{cm}^3$ at 25°C . When the electron carrier concentration is reduced to $10^{17} / \text{cm}^3$ or less and more preferably to $10^{16} / \text{cm}^3$ or less, TFTs of a normally off type can be obtained in high yield. The electron carrier concentration can be determined by hall-effect measurement.

[0031] In the present invention, "amorphous oxide" is defined as an oxide that shows a halo pattern in an X-ray diffraction spectrum and exhibits no particular diffraction line. The lower limit of the electron carrier concentration of the amorphous oxide of the present invention is not particularly limited as long as the oxide can be used as the TFT channel layer. The lower limit is, for example, $10^{12} / \text{cm}^3$.

[0032] Thus, in the present invention, the starting materials, composition ratio, production conditions, and the like of the amorphous oxide are controlled as in the individual examples described below so as to adjust the electron carrier concentration to $10^{12} / \text{cm}^3$ or more but less than $10^{18} / \text{cm}^3$. Preferably, the electron carrier concentration is adjusted to $10^{13} / \text{cm}^3$ to $10^{17} / \text{cm}^3$, and more preferably $10^{15} / \text{cm}^3$ to $10^{16} / \text{cm}^3$.

[0033] The electron mobility is preferably $0.1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ or more, more preferably $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ or more, and most preferably $5 \text{ cm}^2/(\text{V}\cdot\text{sec})$ or more when measured at room temperature. The amorphous oxide

exhibits increased electron mobility as the electron carrier concentration increases. The conductivity thereof tends to exhibit degenerate conduction. Degenerate conduction is defined as a state in which the thermal activation energy for temperature dependency of electrical resistance is 30 meV or less.

(Starting materials for amorphous oxide)

[0034] The amorphous oxide of the present invention as defined in claim 1.

[0035] The amorphous oxide described herein is a unitary, binary, or ternary compound within a triangle with apexes of SnO₂, In₂O₃, and ZnO. Among these three compounds, In₂O₃ has high amorphous formation capacity and can form a completely amorphous phase when In₂O₃ is deposited by a vapor phase method while adding approximately 0.1 Pa of water into the atmosphere.

[0036] ZnO and SnO₂ in some cases do not form an amorphous phase by themselves; however, they can form an amorphous phase in the presence of In₂O₃ as a host oxide. In particular, of binary compositions containing two of the above-described three compounds (compositions located on the side of the triangle), the In-Zn-O system can form an amorphous film when In is contained in an amount of about 20 at% or more, and the Sn-In-O system can form an amorphous film when In is contained in an amount of about 80 at% or more by a vapor phase method.

[0037] In order to obtain an In-Zn-O amorphous film by a vapor phase method, about 0.1 Pa of steam may be introduced into the atmosphere. In order to obtain an In-Sn-O-system amorphous film by a vapor phase method, about 0.1 Pa of nitrogen gas may be introduced into the atmosphere. For the ternary composition, Sn-In-Zn, containing the three compounds, an amorphous film can be obtained by a vapor phase method when In is contained in an amount of about 15 at% in the above-described composition range. Note that "at%" herein indicates atomic percent with respect to the metal ions other than oxygen ions. In particular, for example, "the In-Zn-O system containing about 20 at% or more of In" is equivalent to In_xZn_{1-x} (x > 0.2).

[0038] Further described are compositions of the amorphous oxide film containing Sn, In, and/or Zn, which may contain additional elements as described below. In particular, at least one element that forms a compound oxide, the at least one element being selected from a group 2 element M2 (M2: Mg or Ca) having an atomic number smaller than that of Zn, Lu or a group 3 element M3 (M3: B, Al, Ga, or Y) having an atomic number smaller than that of In, and a group 4 element M4 (M4: Si, Ge, or Zr) having an atomic number smaller than that of Sn may be added. The amorphous oxide film of the present invention may further contain at least one element that can form a compound oxide, the at least one element being selected from group 5 elements (M5: V, Nb, and

Ta) and W.

[0039] Addition of the above-described elements will increase the stability of the amorphous film and expands the composition range that can give an amorphous film. In particular, addition of highly covalent B, Si, or Ge is effective for stabilization of the amorphous phase, and a compound oxide composed of ions with largely different ion radii can stabilize the amorphous phase. For example, in the In-Zn-O system, a stable amorphous film is rarely obtained at room temperature unless the range of In content is more than about 20 at%. However, by adding Mg in an equivalent amount to In, a stable amorphous film can be obtained at an In content of more than about 15 at%.

[0040] An example of the amorphous oxide material that can be used in the channel layer of the TFT of the present invention is described next. The amorphous oxide that can be used in the channel layer is, for example, an oxide that contains In, Ga, and Zn at an atomic ratio satisfying In:Ga:Zn = 1:1:m, wherein m is a value less than 6. The value of m may be a natural number but is not necessarily a natural number. This applies to "m" referred to in other sections of this description. The atomic ratio can be considered as equivalent to a molar ratio.

[0041] A transparent amorphous oxide thin film whose composition in a crystallized state is represented by InGaO₃(ZnO)_m (wherein m is a number less than 6) maintains a stable amorphous state at high temperatures not less than 800°C when the value of m is less than 6. However, as the value of m increases, i.e., as the ratio of ZnO to InGaO₃ increases and the composition approaches to the ZnO composition, the composition tends to be more crystallizable. Thus, the value of m is preferably less than 6 for the channel layer of the amorphous TFT. A desired amorphous oxide can be obtained by adjusting the composition of the target material (e.g., a polycrystalline material) for deposition, such as sputtering deposition or pulsed laser deposition (PLD), to comply with m < 6.

[0042] In the amorphous oxide described above, Zn in the composition ratio of InGaZn may be replaced by Zn_{1-x}Mg_x. The possible amount of Mg for replacement is within the range of 0 < x ≤ 1. When the replacement with Mg is conducted, the electron mobility of the oxide film decreases compared to a film containing no Mg. However, the extent of decrease is small, and the electron carrier concentration can be decreased compared to when no replacement is conducted.

[0043] Thus, this is more preferable for the channel layer of a TFT. The amount of Mg for replacement is preferably more than 20% and less than 85% (0.2 < x < 0.85 in term of x) and more preferably 0.5 < x < 0.85.

[0044] Further described are amorphous oxide, which may be appropriately selected from In oxides, In_xZn_{1-x} oxides (0.2 ≤ x ≤ 1), In_xSn_{1-x} oxides (0.8 ≤ x ≤ 1), and In_x(Zn, Sn)_{1-x} oxides (0.15 ≤ x ≤ 1). The ratio of Zn to Sn in the In_x(Zn, Sn)_{1-x} oxides may be appropriately selected. Namely, an In_x(Zn, Sn)_{1-x} oxide can be described as In_x(ZnySn_{1-y})_{1-x} oxide, and y is in the range of 1 to 0. For

an In oxide containing neither Zn nor Sn, In may be partly replaced by Ga. In this case, the oxide can be described as an $\text{In}_x\text{Ga}_{1-x}$ oxide ($0 \leq x \leq 1$).

(Method for producing amorphous oxide)

[0045] The amorphous oxide used in the present invention can be prepared by a vapor phase deposition technique under the conditions indicated in the individual examples below. For example, in order to obtain an In-GaZn amorphous oxide, deposition is conducted by a vapor phase method such as a sputtering (SP) method, a pulsed laser deposition (PLD) method, or an electron beam deposition method while using a polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_m$ as the target. From the standpoint of mass productivity, the sputtering method is most suitable.

[0046] During the formation of an In_2O_3 or In-Zn-O amorphous oxide film or the like, oxygen radicals may be added to the atmosphere. Oxygen radicals may be added through an oxygen radical generator. When there is need to increase the electron carrier concentration after the film formation, the film is heated in a reducing atmosphere to increase the electron carrier concentration. The resulting amorphous oxide film with a different electron carrier concentration was analyzed to determine the dependency of the electron mobility on the electron carrier concentration, and the electron mobility increased with the electron carrier concentration.

(Substrate)

[0047] The substrate for forming the TFT of the present invention may be a glass substrate, a plastic substrate, a plastic film, or the like. Moreover, as described below in EXAMPLES, the amorphous oxide of the present invention can be formed into a film at room temperature. Thus, a TFT can be formed on a flexible material such as a PET film. Moreover, the above-mentioned amorphous oxide may be appropriately selected to prepare a TFT from a material that is transparent in visible light not less than 400 nm or infrared light.

(Gate insulating film)

[0048] The gate insulating film of the TFT of the present invention is preferably a gate insulating film composed of Al_2O_3 , Y_2O_3 , HfO_2 , or a mixed crystal compound containing at least two of these compounds. When there is a defect at the interface between the gate insulating thin film and the channel layer thin film, the electron mobility decreases and hysteresis occurs in the transistor characteristics. Moreover, leak current greatly differs according to the type of the gate insulating film. Therefore, a gate insulating film suitable for the channel layer must be selected.

[0049] Use of an Al_2O_3 film can decrease the leak current. Use of an Y_2O_3 film can reduce the hysteresis. Use

of a high dielectric constant HfO_2 film can increase the field effect mobility. By using a film composed of a mixed crystal of these compounds, a TFT having small leak current and hysteresis and large field effect mobility can be produced the process for forming the gate insulating film and the process for forming the channel layer can be conducted at room temperature; thus, a TFT of a staggered or inverted staggered structure can be formed.

10 (Transistor)

[0050] When a field effect transistor includes a channel layer composed of an amorphous oxide film having an electron carrier concentration of less than $10^{18}/\text{cm}^3$, a source terminal, a drain terminal, and a gate terminal disposed on the gate insulating film, the current between the source and drain terminals can be adjusted to about 10^{-7} A when a voltage of about 5V is applied between the source and drain terminals without application of a gate voltage. The theoretical lower limit of the electron carrier concentration is $10^5/\text{cm}^3$ or less assuming that the electrons in the valence band are thermally excited. The actual possibility is that the lower limit is about $10^{12}/\text{cm}^3$.

20 **[0051]** When Al_2O_3 , Y_2O_3 , or HfO_2 alone or a mixed crystal compound containing at least two of these compounds is used in the gate insulating layer, the leak voltage between the source gate terminals and the leak voltage between the drain and gate terminals can be adjusted to about 10^{-7} A, and a normally off transistor can be realized.

30 **[0052]** The electron mobility of the oxide crystals increases as the overlap of the s orbits of the metal ion increases. The oxide crystals of Zn, In, and Sn having large atomic numbers exhibit high electron mobility of 0.1 to 200 $\text{cm}^2/(\text{V}\cdot\text{sec})$. Since ionic bonds are formed between oxygen and metal ions in an oxide, electron mobility substantially comparable to that in a crystallized state can be exhibited in an amorphous state in which there is no directionality of chemical bonding, the structure is random, and the directions of the bonding are non-uniform. In contrast, by replacing Zn, In, and Sn each with an element having a smaller atomic number, the electron mobility can be decreased. Thus, by using the amorphous oxide described above, the electron mobility can be controlled within the range of about 0.01 $\text{cm}^2/(\text{V}\cdot\text{sec})$ to 20 $\text{cm}^2/(\text{V}\cdot\text{sec})$.

40 **[0053]** In a typical compound, the electron mobility decreases as the carrier concentration increases due to the dispersion between the carriers. In contrast, the amorphous oxide of the present invention exhibits increased electron mobility with the increasing electron carrier concentration. The physical principle that lies behind this phenomenon is not clearly identified.

50 **[0054]** Once a voltage is applied to the gate terminal, electrons are injected into the amorphous oxide channel layer, and current flows between the source and drain terminals, thereby allowing the part between the source

and drain terminals to enter an ON state. According to the amorphous oxide film of the present invention, since the electron mobility increases with the electron carrier concentration, the current that flows when the transistor is turned ON can be further increased. In other words, the saturation current and the on/off ratio can be further increased. When the amorphous oxide film having high electron mobility is used as the channel layer of a TFT, the saturation current can be increased and the switching rate of the TFT can be increased, thereby achieving high-speed operation.

[0055] For example, when the electron mobility is about $0.01 \text{ cm}^2/(\text{V}\cdot\text{sec})$, the material can be used in a channel layer of a TFT for driving a liquid crystal display element. By using an amorphous oxide film having an electron mobility of about $0.1 \text{ cm}^2/(\text{V}\cdot\text{sec})$, a TFT that has performance comparable or superior to the TFT using an amorphous silicon film and that can drive a display element for moving images can be produced.

[0056] In order to realize a TFT that requires large current, e.g., for driving a current-driven organic light-emitting diode, the electron mobility is preferably more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$. Note that when the amorphous oxide of the present invention that exhibits degenerate conduction is used in the channel layer, the current that flows at a high carrier concentration, i.e., the saturation current of the transistor, shows decreased dependency on temperature, and a TFT with superior temperature characteristics can be realized.

EXAMPLES

(EXAMPLE 1: Preparation of amorphous In-Ga-Zn-O thin film by PLD method)

[0057] A film was formed in a PLD device shown in Fig. 7. In the drawing, reference numeral 701 denotes a rotary pump (RP), 702 denotes a turbo molecular pump (TMP), 703 denotes a preparation chamber, 704 denotes an electron gun for RHEED, 705 denotes a substrate holder for rotating and vertically moving the substrate, 706 denotes a laser entrance window, 707 denotes a substrate, 708 denotes a target, 709 denotes a radical source, 710 denotes a gas inlet, 711 denotes a target holder for rotating and vertically moving the target, 712 denotes a bypass line, 713 denotes a main line, 714 denotes a turbo molecular pump (TMP), 715 denotes a rotary pump (RP), 716 denotes a titanium getter pump, and 717 denotes a shutter. In the drawing, 718 denotes ionization gauge (IG), 719 denotes a Pirani gauge (PG), 720 denotes a Baratron gauge (BG), and 721 denotes a deposition chamber.

[0058] An In-Ga-Zn-O amorphous oxide semiconductor thin film was formed on a SiO_2 glass substrate (# 1737 produced by Corning) by a pulsed laser deposition method using a KrF excimer laser. As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes

each, and then dried in air at 100°C .

[0059] An $\text{InGaO}_3(\text{ZnO})_4$ sinter target (size: 20 mm in dia., 5 mm in thickness) was used as the polycrystalline target. This target was prepared by wet-mixing the starting materials, $\text{In}_2\text{O}_3\text{:Ga}_2\text{O}_3\text{:ZnO}$ (each being a 4N reagent), in a solvent (ethanol), calcining (1000°C , 2 h) the resulting mixture, dry-milling the calcined mixture, and sintering the resulting mixture (1550°C , 2 h). The electrical conductivity of the target obtained was 90 (S/cm) .

[0060] The ultimate vacuum of the deposition chamber was adjusted to $2 \times 10^{-6} \text{ (Pa)}$, and the oxygen partial pressure during the deposition was controlled to 6.5 (Pa) to form a film. The oxygen partial pressure inside the chamber 721 was 6.5 Pa , and the substrate temperature was 25°C . The distance between the target 708 and the substrate 707 for deposition was 30 (mm) . The power of the KrF excimer laser entering from the entrance window 716 was in the range of $1.5 \text{ to } 3 \text{ (mJ/cm}^2\text{/pulse)}$. The pulse width was 20 (nsec) , the repetition frequency was 10 (Hz) , and the beam spot diameter was $1 \times 1 \text{ (mm square)}$. A film was formed at a deposition rate of 7 (nm/min) .

[0061] The resulting thin film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°), but no clear diffraction peak was observed. Thus, the In-Ga-Zn-O thin film obtained was assumed to be amorphous. The X-ray reflectance was determined, and the pattern was analyzed. It was observed that the root mean square roughness (R_{rms}) of the thin film was about 0.5 nm , and the film thickness was about 120 nm . The results of the fluorescence X-ray showed that the metal composition ratio of the thin film was $\text{In:Ga:Zn} = 0.98:1.02:4$. The electrical conductivity was less than about 10^{-2} S/cm . The electron carrier concentration and the electron mobility were presumably about $10^{16} / \text{cm}^3$ or less and about $5 \text{ cm}^2/(\text{V}\cdot\text{sec})$, respectively.

[0062] Based on the analysis of the optical absorption spectrum, the energy width of the forbidden band of the amorphous thin film prepared was determined to be about 3 eV . Based on these values, it was found that the In-Ga-Zn-O thin film had an amorphous phase close to the composition of the crystals of $\text{InGaO}_3(\text{ZnO})_4$, had fewer oxygen defects, and was a flat, transparent thin film with low electrical conductivity.

[0063] Specific description is now presented with reference to Fig. 1. Fig. 1 shows a change in electron carrier concentration of the oxide formed into a film against changes in oxygen partial pressure when an In-Ga-Zn-O transparent amorphous oxide thin film represented by $\text{InGaO}_3(\text{ZnO})_4$ in an assumed crystal state is formed under the same conditions as in this EXAMPLE.

[0064] As shown in Fig. 1, the electron carrier concentration decreased to less than $10^{18} / \text{cm}^3$ when the film was formed in an atmosphere at a high oxygen partial pressure of more than 4.5 Pa under the same conditions as this example. In this case, the temperature of the substrate was maintained substantially at room temperature without intentional heating. The substrate temperature is

preferably less than 100°C when a flexible plastic film is used as the substrate.

[0065] By further increasing the oxygen partial pressure, the electron carrier concentration was further decreased. For example, as shown in Fig. 1, the number of the electron carriers of the $\text{InGaO}_3(\text{ZnO})_4$ thin film deposited at a substrate temperature of 25°C and an oxygen partial pressure of 5 Pa decreased to $10^{16}/\text{cm}^3$.

[0066] The thin film obtained had an electron mobility exceeding $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$, as shown in Fig. 2. However, according to the pulsed laser deposition method of the present invention, the surface of the film deposited will have irregularities at an oxygen partial pressure of 6.5 Pa or more, and thus, it is difficult to use the thin film as a channel layer of a TFT. Therefore, by using an In-Ga-Zn-O transparent amorphous oxide thin film having a composition of $\text{InGaO}_3(\text{ZnO})_m$ (m is less than 6) in a crystal state prepared by a pulsed laser deposition method in an atmosphere having an oxygen partial pressure exceeding 4.5 Pa, preferably exceeding 5 Pa, but less than 6.5 Pa, a normally off transistor can be prepared.

[0067] The electron mobility of this thin film was more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$, and the on/off ratio thereof was increased to over 10^3 . As is described above, in forming an InGaZn oxide film by a PLD method under the conditions set forth in this example, the oxygen partial pressure is preferably controlled to not less than 4.5 Pa but less than 6.5 Pa. Whether an electron carrier concentration of $10^{18}/\text{cm}^3$ is realized depends on the conditions of the oxygen partial pressure, the configuration of the deposition device, the materials for deposition, the composition, and the like.

[0068] Next, in the above-described device at an oxygen partial pressure of 6.5 Pa, an amorphous oxide was made and a top-gate MISFET element shown in Fig. 5 was formed. In particular, a semi-insulating amorphous $\text{InGaO}_3(\text{ZnO})_9$ film having a thickness of 120 nm for use as a channel layer (2) was formed on a glass substrate (1) by the above-described method for making the amorphous In-Ga-Zn-O thin film.

[0069] On this film, $\text{InGaO}_3(\text{ZnO})_4$ having a high electrical conductivity and a gold film each 30 nm in thickness were deposited by a pulsed laser deposition method while controlling the oxygen partial pressure inside the chamber to less than 1 Pa. A drain terminal (5) and a source terminal (6) were formed by a photolithographic method and a lift-off method.

[0070] Lastly, an Y_2O_3 film (thickness: 90 nm, relative dielectric constant: about 15, leak current density: 10^{-3} A/cm^2 upon application of 0.5 MV/cm) for use as a gate insulating film (3) was deposited by an electron beam deposition method, and gold was deposited on the Y_2O_3 film. A gate terminal (4) was formed by a photolithographic method and a lift-off method.

(Evaluation of characteristics of MISFET element)

[0071] Fig. 6 shows the current-voltage characteristics

of MISFET elements measured at room temperature. Since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was proved to be an n-type semiconductor. This is consistent with the fact that the amorphous In-Ga-Zn-O semiconductor is of an n-type. I_{DS} was saturated (pinch-off) at $V_{\text{DS}} =$ about 6 V, which was a typical behavior for semiconductor transistors. The gain characteristic was determined, and the threshold value of the gate voltage V_{GS} when $V_{\text{DS}} = 4 \text{ V}$ was applied was about -0.5 V. Upon application of $V_{\text{G}} = 10 \text{ V}$, current of $I_{\text{DS}} = 1.0 \times 10^{-5} \text{ A}$ flowed. This is because carriers were induced in the In-Ga-Zn-O amorphous semiconductor thin film, i.e., an insulator, due to the gate bias. The on/off ratio of the transistor exceeded 10^3 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $7 \text{ cm}^2(\text{VS})^{-1}$ was obtained in the saturation region.

[0072] The same measurements were carried out on the element while irradiating the element with visible light, but no change in transistor characteristics was observed. According to the present example, a thin film transistor having a channel layer exhibiting a low electron carrier concentration, a high electrical resistance, and high electron mobility can be realized. Note that the above-described amorphous oxide showed excellent characteristics in that the electron mobility increased with the electron carrier concentration and that degenerate conduction was exhibited.

[0073] In this example, the thin film transistor was formed on the glass substrate. Since the film can be formed at room temperature, a substrate such as a plastic board or a film can be used. The amorphous oxide obtained in this example absorbs little visible light; thus, a transparent, flexible TFT can be made.

(EXAMPLE 2: Formation of amorphous $\text{InGaO}_3(\text{ZnO})$ and $\text{InGaO}_3(\text{ZnO})_4$ oxide films by PLD method)

[0074] In-Zn-Ga-O amorphous oxide films were deposited on glass substrates (#1737 produced by Corning) by using polycrystalline sinters represented by $\text{InGaO}_3(\text{ZnO})$ and $\text{InGaO}_3(\text{ZnO})_4$ as the targets by a PLD method using KrF excimer laser. The same PLD deposition device as shown in EXAMPLE 1 was used, and the deposition was conducted under the same conditions. The substrate temperature during the deposition was 25°C.

[0075] Each film obtained thereby was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°) for the film surface. No clear diffraction peak was detected. The In-Zn-Ga-O films prepared from the two targets were both amorphous.

[0076] The In-Zn-Ga-O amorphous oxide films on the glass substrates were each analyzed to determine the x-ray reflectance. Analysis of the pattern found that the root mean average roughness (R_{rms}) of the thin film was about 0.5 nm and that the thickness was about 120 nm. Fluorescence x-ray analysis (XRF) showed that the ratio

of the metal atoms of the film obtained from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})$ was $\text{In:Ga:Zn} = 1.1:1.1:0.9$ and that the ratio of the metal atoms of the film obtained from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_4$ was $\text{In:Ga:Zn} = 0.98:1.02:4$.

[0077] The electron carrier concentration of the amorphous oxide film obtained from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_4$ was measured while changing the oxygen partial pressure of the atmosphere during the deposition. The results are shown in Fig. 1. By forming the film in the atmosphere having an oxygen partial pressure exceeding 4.5 Pa, the electron carrier concentration could be decreased to less than $10^{18}/\text{cm}^3$. In this case, the temperature of the substrate was maintained substantially at room temperature without intentional heating. When the oxygen partial pressure was less than 6.5 Pa, the surface of the amorphous oxide film obtained was flat.

[0078] When the oxygen partial pressure was 5 Pa, the electron carrier concentration and the electrical conductivity of the amorphous oxide film obtained from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_4$ were $10^{16}/\text{cm}^3$ and 10^{-2} S/cm , respectively. The electron mobility was presumably about $5 \text{ cm}^2/(\text{V}\cdot\text{sec})$. Based on the analysis of the optical absorption spectrum, the energy width of the forbidden band of the amorphous thin film prepared was determined to be about 3 eV. The electron carrier concentration could be further decreased as the oxygen partial pressure was increased from 5 Pa.

[0079] As shown in Fig. 1, the In-Zn-Ga-O amorphous oxide film deposited at a substrate temperature of 25°C and an oxygen partial pressure of 6 Pa exhibited a decreased electron carrier concentration of $8 \times 10^{15}/\text{cm}^3$ (electrical conductivity: about $8 \times 10^{-3} \text{ S/cm}$). The resulting film was assumed to have an electron mobility of more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$. However, according to the PLD method, irregularities were formed in the surface of the film deposited at an oxygen partial pressure of 6.5 Pa or more, and thus it was difficult to use the film as the channel layer of the TFT.

[0080] The relationship between the electron carrier concentration and the electron mobility of the In-Zn-Ga-O amorphous oxide film prepared from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_4$ at different oxygen partial pressures was investigated. The results are shown in Table 2. When the electron carrier concentration increased from $10^{16}/\text{cm}^3$ to $10^{20}/\text{cm}^3$, the electron mobility increased from about $3 \text{ cm}^2/(\text{V}\cdot\text{sec})$ to about $11 \text{ cm}^2/(\text{V}\cdot\text{sec})$. The same tendency was observed for the amorphous oxide film prepared from the target composed of the polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})$.

[0081] An In-Zn-Ga-O amorphous oxide film formed on a polyethylene terephthalate (PET) film having a thickness of $200 \mu\text{m}$ instead of the glass substrate also showed similar characteristics.

(EXAMPLE 3: Formation of In-Zn-Ga-O amorphous oxide film by SP method)

[0082] Formation of a film by a high-frequency SP method using argon gas as the atmosphere gas is described. The SP method was conducted using the device shown in Fig. 8. In the drawing, reference numeral 807 denotes a substrate for deposition, 808 denotes a target, 805 denotes a substrate holder equipped with a cooling mechanism, 814 denotes a turbo molecular pump, 815 denotes a rotary pump, 817 denotes a shutter, 818 denotes an ionization gauge, 819 denotes a Pirani gauge, 821 denotes a deposition chamber, and 830 denotes a gate valve. A SiO_2 glass substrate (#1737 produced by Corning) was used as the substrate 807 for deposition. As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes each, and then dried in air at 100°C .

[0083] An $\text{InGaO}_3(\text{ZnO})_4$ polycrystalline sinter (size: 20 mm in dia., 5 mm in thickness) was used as the target material. The sinter was prepared by wet-mixing the starting materials, $\text{In}_2\text{O}_3:\text{Ga}_2\text{O}_3:\text{ZnO}$ (each being a 4N reagent), in a solvent (ethanol), calcining (1000°C , 2 h) the resulting mixture, dry-milling the calcined mixture, and sintering the resulting mixture (1550°C , 2 h). The target 808 had an electrical conductivity of 90 (S/cm) and was in a semi-insulating state.

[0084] The ultimate vacuum inside the deposition chamber 821 was 1×10^{-4} (Pa). The total pressure of the oxygen gas and the argon gas during the deposition was controlled at a predetermined value within the range of 4 to 0.1×10^{-1} (Pa), and the oxygen partial pressure was changed in the range of 10^{-3} to 2×10^{-1} (Pa) by changing the partial pressure ratio of the argon gas and oxygen. The substrate temperature was room temperature, and the distance between the target 808 and the substrate 807 for deposition was 30 (mm). The current injected was RF 180 W, and the deposition rate was 10 (nm/min).

[0085] The resulting film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle $\text{lang} = \text{EN} - \text{US} > 0.5^\circ$) for the film surface, but no clear diffraction peak was observed. Thus, the In-Zn-Ga-O thin film obtained was proved to be amorphous. The X-ray reflectance was determined, and the pattern was analyzed.

[0086] It was observed that the root mean square roughness (R_{rms}) of the thin film was about 0.5 nm, and the film thickness was about 120 nm. The results of the fluorescence X-ray showed that the metal composition ratio of the thin film was $\text{In:Ga:Zn} = 0.98:1.02:4$.

[0087] The electrical conductivity of the amorphous oxide film obtained by changing the oxygen partial pressure in the atmosphere during the deposition was measured. The results are shown in Fig. 3. As shown in Fig. 3, the electrical conductivity could be decreased to less than 10 S/cm by forming the film in an atmosphere at a high

oxygen partial pressure exceeding 3×10^{-2} Pa.

[0088] By further increasing the oxygen partial pressure, the number of electron carriers could be decreased. For example, as shown in Fig. 3, the electrical conductivity of an $\text{InGaO}_3(\text{ZnO})_4$ thin film deposited at a substrate temperature of 25°C and an oxygen partial pressure of 10^{-1} Pa was decreased to about 10^{-10} S/cm. An $\text{InGaO}_3(\text{ZnO})_4$ thin film deposited at an oxygen partial pressure exceeding 10^{-1} Pa had excessively high electrical resistance and thus the electrical conductivity thereof could not be measured. However, extrapolation was conducted for the value observed from a film having a high electron carrier concentration, and the electron mobility was assumed to be about $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$.

[0089] In short, a normally off transistor having an on/off ratio exceeding 10^3 could be made by using a transparent amorphous oxide thin film which was composed of In-Ga-Zn-O prepared by a sputter deposition method in argon gas atmosphere at an oxygen partial pressure more than 3×10^{-2} Pa, preferably more than 5×10^{-1} Pa, and which was represented by $\text{InGaO}_3(\text{ZnO})_4$ (m is a natural number less than 6) in a crystallized state.

[0090] When the device and starting materials set forth in this example are used, the oxygen partial pressure during the sputter deposition is, for example, in the range of 3×10^{-2} Pa to 5×10^{-1} Pa. The electron mobility of the thin films prepared by the pulsed laser deposition method and the sputtering method increases with the number of the conduction electrons, as shown in Fig. 2.

[0091] As described above, by controlling the oxygen partial pressure, oxygen defects can be reduced, and therefore the electron carrier concentration can be reduced. Unlike in the polycrystalline state, in the amorphous state, there is essentially no grain interface; therefore, an amorphous thin film with high electron mobility can be obtained. Note that when a polyethylene terephthalate (PET) film having a thickness of $200 \mu\text{m}$ was used instead of the glass substrate, the resulting $\text{InGaO}_3(\text{ZnO})_4$ amorphous oxide thin film exhibited similar characteristics.

(EXAMPLE 4: Formation of In-Zn-Ga-Mg-O amorphous oxide film by PLD method, Reference Example)

[0092] Formation of an $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_4$ film ($0 < x < 1$) on a glass substrate by a PLD method is described. The same deposition device shown in Fig. 7 was used as the deposition device. A SiO_2 glass substrate (#1737 produced by Corning) was prepared as the substrate for deposition. As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes each, and then dried in air at 100°C .

[0093] An $\text{InGa}(\text{Zn}_{1-x}\text{Mg}_x\text{O})_4$ ($0 < x < 1$) sinter (size: 20 mm in dia., 5 mm in thickness) was used as the target. The target was prepared by wet-mixing the starting materials, In_2O_3 : Ga_2O_3 : ZnO : MgO (each being a 4N reagent), in a solvent (ethanol), calcining (1000°C , 2 h) the

resulting mixture, dry-milling the calcined mixture, and sintering the resulting mixture (1550°C , 2 h).

[0094] The ultimate vacuum inside the deposition chamber was 2×10^{-6} (Pa), and the oxygen partial pressure during the deposition was 0.8 (Pa). The substrate temperature was room temperature (25°C), and the distance between the target and the substrate for deposition was 30 (mm). The power of the KrF excimer laser was $1.5 \text{ (mJ/cm}^2/\text{pulse)}$, the pulse width was 20 (nsec), the repetition frequency was 10 (Hz), and the beam spot diameter was 1×1 (mm square). The deposition rate was 7 (nm/min).

[0095] The resulting film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°) for the film surface, but no clear diffraction peak was observed. Thus, the In-Zn-Ga-Mg-O thin film obtained was proved to be amorphous. The surface of the resulting film was flat.

[0096] The dependency on the value x of the electrical conductivity, electron carrier concentration, and electron mobility of In-Zn-Ga-Mg-O amorphous oxide films deposited in atmosphere at an oxygen partial pressure of 0.8 Pa was investigated by using targets of different x values. Note that a high-resistance amorphous $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_m$ film could be obtained at an oxygen partial pressure of less than 1 Pa as long as the polycrystalline $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_m$ (m is a natural number less than 6; $0 < x \leq 1$) was used as the target.

[0097] The results are shown in Fig. 4. The results showed that the electron carrier concentration of an amorphous oxide film deposited by a PLD method in an atmosphere at an oxygen partial pressure of 0.8 Pa could be reduced to less than $10^{18} / \text{cm}^3$ when the value x was more than 0.4. The electron mobility of the amorphous oxide film with x exceeding 0.4 was more than $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$. As shown in Fig. 4, when a target in which Zn was substituted with 80 at% Mg was used, the electron carrier concentration of the film obtained by the pulsed laser deposition method in an atmosphere at an oxygen partial pressure of 0.8 Pa could be reduced to less than $10^{16} / \text{cm}^3$.

[0098] Although the electron mobility of these films is low compared to that of Mg-free films, the degree of decrease is small, while the electron mobility at room temperature is about $5 \text{ cm}^2/(\text{V}\cdot\text{sec})$, i.e., higher than that of amorphous silicon by one order of magnitude. When deposition is conducted under the same conditions, the electrical conductivity and the electron mobility both decrease with an increase in Mg content. Thus, the Mg content is preferably more than 20 at% but less than 85 at% ($0.2 < x < 0.85$ in terms of x), and more preferably $0.5 < x < 0.85$.

[0099] An $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_4$ ($0 < x < 1$) amorphous oxide film formed on a polyethylene terephthalate (PET) film having a thickness of $200 \mu\text{m}$ instead of the glass substrate also showed similar characteristics.

(EXAMPLE 5: Formation of In_2O_3 amorphous oxide film by PLD, Reference Example)

[0100] Formation of an indium oxide film is now described.

[0101] The deposition device shown in Fig. 7 was used as the deposition device. A SiO_2 glass substrate (#1737 produced by Corning) was prepared as the substrate for deposition.

[0102] As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes each, and then dried in air at 100°C .

[0103] An In_2O_3 sinter (size: 20 mm in dia., 5 mm in thickness) was used as the target. The target was prepared by calcining the starting material In_2O_3 (a 4N reagent) (1000°C , 2 h), dry milling the calcined material, and sintering the resulting material (1550°C , 2 h).

[0104] The ultimate vacuum inside the deposition chamber was 2×10^{-6} (Pa), and the oxygen partial pressure during the deposition was 5 (Pa). The steam partial pressure was 0.1 (Pa), and 200 W was applied to the oxygen radical generator to produce oxygen radicals. The substrate temperature was room temperature. The distance between the target and the substrate for deposition was 40 (mm). The power of the KrF excimer laser was $0.5 \text{ (mJ/cm}^2\text{/pulse)}$, the pulse width was 20 (nsec), the repetition frequency was 10 (Hz), and the beam spot diameter was 1×1 (mm square). The deposition rate was 3 (nm/min).

[0105] The resulting film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°) for the film surface, but no clear diffraction peak was observed. Thus, the In-O thin film obtained was proved to be amorphous. The film thickness was 80 nm. The electron carrier concentration and the electron mobility of the In-O amorphous oxide film obtained were $5 \times 10^{17} \text{ /cm}^3$ and about $7 \text{ cm}^2\text{/(V}\cdot\text{sec)}$, respectively.

(EXAMPLE 6: Formation of In-Sn-O amorphous oxide film by PLD, Reference Example)

[0106] Deposition of an In-Sn-O amorphous oxide film having a thickness of $200 \mu\text{m}$ by a PLD method is described. A SiO_2 glass substrate (#1737 produced by Corning) was prepared as the substrate for deposition. As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes each, and then dried in air at 100°C .

[0107] An $\text{In}_2\text{O}_3\text{-SnO}_2$ sinter (size: 20 mm in dia., 5 mm in thickness) was prepared as the target by wet-mixing the starting materials, $\text{In}_2\text{O}_3\text{-SnO}_2$ (a 4N reagent), in a solvent (ethanol), calcining the resulting mixture (1000°C , 2 h), dry milling the calcined mixture, and sintering the resulting mixture (1550°C , 2 h). The composition of the target was $(\text{In}_{0.9}\text{Sn}_{0.1})_2\text{O}_{3.1}$ polycrystal.

[0108] The ultimate vacuum inside the deposition

chamber was 2×10^{-6} (Pa), the oxygen partial pressure during the deposition was 5 (Pa), and the nitrogen partial pressure was 0.1 (Pa). Then 200 W is applied to the oxygen radical generator to produce oxygen radicals.

5 The substrate temperature during the deposition was room temperature. The distance between the target and the substrate for deposition was 30 (mm). The power of the KrF excimer laser was $1.5 \text{ (mJ/cm}^2\text{/pulse)}$, the pulse width was 20 (nsec), the repetition frequency was 10 (Hz), and the beam spot diameter was 1×1 (mm square).

[0109] The deposition rate was 6 (nm/min). The resulting film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°) for the film surface, but no clear diffraction peak was observed.

15 Thus, the In-Sn-O thin film obtained was proved to be amorphous. The electron carrier concentration and the electron mobility of the In-Sn-O amorphous oxide film obtained were $8 \times 10^{17} \text{ /cm}^3$ and about $5 \text{ cm}^2\text{/(V}\cdot\text{sec)}$, respectively. The film thickness was 100 nm.

(EXAMPLE 7: Formation of In-Ga-O amorphous oxide film by PLD method, Reference Example)

[0110] Deposition of an indium gallium oxide is described next. A SiO_2 glass substrate (#1737 produced by Corning) was prepared as the substrate for deposition. As the pre-deposition treatment, the substrate was degreased with ultrasonic waves in acetone, ethanol, and ultrapure water for 5 minutes each, and then dried in air at 100°C .

[0111] A $(\text{In}_2\text{O}_3)_{1-x}\text{-(Ga}_2\text{O}_3)_x$ ($x = 0$ to 1) sinter was prepared as the target (size: 20 mm in dia., 5 mm in thickness). For example, when $x = 0.1$, the target was an $(\text{In}_{0.9}\text{Ga}_{0.1})_2\text{O}_3$ polycrystalline sinter. This target was obtained by wet-mixing the starting materials, $\text{In}_2\text{O}_3\text{-Ga}_2\text{O}_3$ (4N reagent), in a solvent (ethanol), calcining the resulting mixture (1000°C , 2 h), dry-milling the calcined mixture, and sintering the resulting mixture (1550°C , 2 h).

[0112] The ultimate vacuum inside the deposition chamber was 2×10^{-6} (Pa), and the oxygen partial pressure during the deposition was 1 (Pa). The substrate temperature during the deposition was room temperature. The distance between the target and the substrate for deposition was 30 (mm). The power of the KrF excimer laser was $1.5 \text{ (mJ/cm}^2\text{/pulse)}$, the pulse width was 20 (nsec), the repetition frequency was 10 (Hz), and the beam spot diameter was 1×1 (mm square). The deposition rate was 6 (nm/min).

[0113] The resulting film was subjected to grazing incidence x-ray diffraction (thin film method, incident angle: 0.5°) for the film surface, but no clear diffraction peak was observed. Thus, the In-Ga-O thin film obtained was proved to be amorphous. The film thickness was 120 nm. The electron carrier concentration and the electron mobility of the In-Ga-O amorphous oxide film obtained were $8 \times 10^{16} \text{ /cm}^3$ and about $1 \text{ cm}^2\text{/(V}\cdot\text{sec)}$, respectively.

(EXAMPLE 8: Preparation of TFT element (glass substrate) using In-Zn-Ga-O amorphous oxide film)

[0114] A top-gate TFT element shown in Fig. 5 was prepared. First, an In-Zn-Ga-O amorphous film 120 nm in thickness for use as a channel layer (2) was formed on a glass substrate (1) by a method of preparing the In-Ga-Zn-O amorphous oxide film according to EXAMPLE 2 at an oxygen partial pressure of 5 Pa while using a polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})_4$ as the target.

[0115] An In-Ga-Zn-O amorphous film having high electrical conductivity and a gold film each 30 nm in thickness were deposited on the In-Ga-Zn-O amorphous film by a PLD method while controlling the oxygen partial pressure inside the chamber to less than 1 Pa, and a drain terminal (5) and a source terminal (6) were formed by a photolithographic method and a lift-off method.

[0116] Lastly, an Y_2O_3 film (thickness: 90 nm, relative dielectric constant: about 15, leak current density: 10^{-3} A/cm² upon application of 0.5 MV/cm) for use as a gate insulating film (3) was formed by an electron beam deposition method, and gold was deposited on the Y_2O_3 film. A gate terminal (4) was formed by a photolithographic method and a lift-off method. The channel length was 50 μm and the channel width was 200 μm .

(Evaluation of characteristics of TFT element)

[0117] Fig. 6 shows the current-voltage characteristic of the TFT element measured at room temperature. Since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was found to be of an n-conductivity type. This is consistent with the fact that the amorphous In-Ga-Zn-O oxide film is an n-type conductor. I_{DS} was saturated (pinch-off) at about $V_{\text{DS}} = 6$ V, which was a typical behavior for semiconductor transistors. The gain characteristic was determined, and the threshold value of the gate voltage V_{GS} when $V_{\text{DS}} = 4$ V was applied was about -0.5 V. Upon application of $V_{\text{G}} = 10$ V, current of $I_{\text{DS}} = 1.0 \times 10^{-5}$ A flowed. This is because carriers were induced in the In-Ga-Zn-O amorphous semiconductor thin film, i.e., an insulator, due to the gate bias. The on/off ratio of the transistor exceeded 10^3 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $7 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0118] The same measurements were carried out on the element while irradiating the element with visible light, but no change in transistor characteristics was observed. Note that the film can be used as a channel layer of a TFT by controlling the electron carrier concentration of the amorphous oxide to less than $10^{18}/\text{cm}^3$. An electron carrier concentration of $10^{17}/\text{cm}^3$ or less was more preferable, and an electron carrier density of $10^{16}/\text{cm}^3$ or less was yet more preferable.

(EXAMPLE 9: Preparation of TFT element using In-Zn-Ga-O amorphous oxide film)

[0119] A top-gate TFT element shown in Fig. 5 was prepared. In particular, an In-Zn-Ga-O amorphous oxide film 120 nm in thickness for use as a channel layer (2) was formed on a polyethylene terephthalate (PET) film (1) by a deposition method of EXAMPLE 2 in an atmosphere at an oxygen partial pressure of 5 Pa using a polycrystalline sinter represented by $\text{InGaO}_3(\text{ZnO})$ as the target.

[0120] An In-Zn-Ga-O amorphous oxide film having high electrical conductivity and a gold film each 30 nm in thickness were deposited on the In-Zn-Ga-O amorphous oxide film by the PLD method at an oxygen partial pressure inside the chamber of less than 1 Pa, and a drain terminal (5) and a source terminal (6) were formed by a photolithographic method and a lift-off method.

[0121] Lastly, a gate insulating film (3) was formed by an electron beam deposition method and gold is deposited thereon. A gate terminal (4) was then formed by a photolithographic method and a lift-off method. The channel length was 50 μm and the channel width was 200 μm . Three types of TFTs with the above-described structure were prepared using Y_2O_3 (thickness: 140 nm), Al_2O_3 (thickness: 130 nm) and HfO_2 (thickness: 140 nm), respectively.

(Evaluation of characteristics of TFT element)

[0122] The current-voltage characteristic of the TFT element measured at room temperature was similar to one shown in Fig. 6. Namely, since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was found to be of an n-conductivity type. This is consistent with the fact that the amorphous In-Ga-Zn-O amorphous oxide film is an n-type conductor. I_{DS} was saturated (pinch-off) at $V_{\text{DS}} =$ about 6 V, which was a typical behavior for semiconductor transistors. When $V_{\text{g}} = 0$ V, current of $I_{\text{ds}} = 10^{-8}$ A flowed, and when $V_{\text{g}} = 10$ V, current of $I_{\text{ds}} = 2.0 \times 10^{-5}$ A flowed. This is because carriers were induced in the In-Ga-Zn-O amorphous oxide thin film, i.e., an insulator, due to the gate bias. The on/off ratio of the transistor exceeded 10^3 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $7 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0123] The element formed on the PET film was inflected at a radius of curvature of 30 mm, and the same transistor characteristic was measured. No change in transistor characteristic was observed.

[0124] The TFT including the gate insulating film made from the Al_2O_3 film also showed similar transistor characteristics to those shown in Fig. 6. When $V_{\text{g}} = 0$ V, current of $I_{\text{ds}} = 10^{-8}$ A flowed, and when $V_{\text{g}} = 10$ V, current of $I_{\text{ds}} = 5.0 \times 10^{-6}$ A flowed. The on/off ratio of the transistor exceeded 10^2 . The field effect mobility was determined from the output characteristics. As a result, a field

effect mobility of about $2 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0125] The TFT including the gate insulating film made from the HfO_2 film also showed similar transistor characteristics to those shown in Fig. 6. When $V_g = 0 \text{ V}$, current of $I_{ds} = 10^{-8} \text{ A}$ flowed, and when $V_g = 10 \text{ V}$, current of $I_{ds} = 1.0 \times 10^{-6} \text{ A}$ flowed. The on/off ratio of the transistor exceeded 10^2 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $10 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

(EXAMPLE 10: Preparation of TFT element using In_2O_3 amorphous oxide film by PLD method, Reference Example)

[0126] A top-gate TFT element shown in Fig. 5 was prepared. First, an In_2O_3 amorphous oxide film 80 nm in thickness for use as a channel layer (2) was formed on a polyethylene terephthalate (PET) film (1) by the deposition method of EXAMPLE 5.

[0127] An In_2O_3 amorphous oxide film having high electrical conductivity and a gold layer each 30 nm in thickness were formed on this In_2O_3 amorphous oxide film by the PLD method at an oxygen partial pressure inside the chamber of less than 1 Pa while applying zero voltage to the oxygen radical generator. A drain terminal (5) and a source terminal (6) were then formed by a photolithographic method and a lift-off method.

[0128] Lastly, an Y_2O_3 film for use as a gate insulating film (3) was formed by an electron beam deposition method, and gold was deposited on the Y_2O_3 film. A gate terminal (4) was formed by a photolithographic method and a lift-off method.

(Evaluation of characteristics of TFT element)

[0129] The current-voltage characteristics of the TFT element formed on the PET film were measured at room temperature. Since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was found to be of an n-conductivity type. This is consistent with the fact that the amorphous In-O amorphous oxide film is an n-type conductor. I_{DS} was saturated (pinch-off) at $V_{DS} =$ about 5 V, which was a typical behavior for semiconductor transistors. When $V_g = 0 \text{ V}$, current of $2 \times 10^{-8} \text{ A}$ flowed, and when $V_g = 10 \text{ V}$, current $I_{DS} = 2.0 \times 10^{-6} \text{ A}$ flowed. This is because carriers were induced in the In-O amorphous oxide thin film, i.e., an insulator, due to the gate bias. The on/off ratio of the transistor was about 10^2 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $10 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0130] The TFT element formed on a glass substrate showed similar characteristics. The element formed on the PET film was inflected at a radius of curvature of 30 mm, and the same transistor characteristics were measured. No change in transistor characteristics was ob-

served.

(EXAMPLE 11: Preparation of TFT element using In-Sn-O amorphous oxide film by PLD method, Reference Example)

[0131] A top gate TFT element shown in Fig. 5 was prepared. In particular, an In-Sn-O amorphous oxide film 100 nm in thickness for use as a channel layer (2) was formed on a polyethylene terephthalate (PET) film (1) by a deposition method of EXAMPLE 6.

[0132] An In-Sn-O amorphous oxide film having high electrical conductivity and a gold film each 30 nm in thickness were deposited on this In-Sn-O amorphous oxide film by the PLD method at an oxygen partial pressure inside the chamber of less than 1 Pa while applying zero voltage to the oxygen radical generator. A drain terminal (5) and a source terminal (6) were formed by a photolithographic method and a lift-off method.

[0133] Lastly, an Y_2O_3 film for use as a gate insulating film (3) was formed by an electron beam deposition method and gold was deposited thereon. A gate terminal (4) was then formed by a photolithographic method and a lift-off method.

(Evaluation of characteristics of TFT element)

[0134] The current-voltage characteristic of the TFT element formed on the PET film was measured at room temperature. Since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was found to be of an n-conductivity type. This is consistent with the fact that the amorphous In-Sn-O amorphous oxide film is an n-type conductor. I_{DS} was saturated (pinch-off) at $V_{DS} =$ about 6 V, which was a typical behavior for semiconductor transistors. When $V_g = 0 \text{ V}$, current of $5 \times 10^{-8} \text{ A}$ flowed, and when $V_g = 10 \text{ V}$, current of $I_{ds} = 5.0 \times 10^{-5} \text{ A}$ flowed. This is because carriers were induced in the In-Sn-O amorphous oxide thin film, i.e., an insulator, due to the gate bias. The on/off ratio of the transistor was about 10^3 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $5 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0135] The TFT element formed on a glass substrate showed similar characteristics. The element formed on the PET film was inflected at a radius of curvature of 30 mm, and the same transistor characteristics were measured. No change in transistor characteristics was observed.

(EXAMPLE 12: Preparation of TFT element using In-Ga-O amorphous oxide film by PLD method Reference Example)

[0136] A top gate TFT element shown in Fig. 5 was prepared.

[0137] In particular, an In-Ga-O amorphous oxide film

120 nm in thickness for use as a channel layer (2) was formed on a polyethylene terephthalate (PET) film (1) by the deposition method of EXAMPLE 7.

[0138] An In-Ga-O amorphous oxide film having high electrical conductivity and a gold film each 30 nm in thickness were formed on this In-Ga-O amorphous oxide film by the PLD method at an oxygen partial pressure inside the chamber of less than 1 Pa while applying zero voltage to the oxygen radical generator. A drain terminal (5) and a source terminal (6) were formed by a photolithographic method and a lift-off method.

[0139] Lastly, an Y_2O_3 film for use as a gate insulating film (3) was formed by an electron beam deposition method and gold was deposited thereon. A gate terminal (4) was then formed by a photolithographic method and a lift-off method.

(Evaluation of characteristics of TFT element)

[0140] The current-voltage characteristic of the TFT element formed on the PET film was measured at room temperature. Since the drain current I_{DS} increased with the drain voltage V_{DS} , the channel was found to be of an n-conductivity type. This is consistent with the fact that the amorphous In-Ga-O amorphous oxide film is an n-type conductor. I_{DS} was saturated (pinch-off) at V_{DS} = about 6 V, which was a typical behavior for semiconductor transistors. When $V_g = 0$ V, current of 1×10^{-8} A flowed, and when $V_g = 10$ V, current of $I_{ds} = 1.0 \times 10^{-6}$ A flowed. This corresponds to the induction of electron carriers inside the insulator, In-Ga-O amorphous oxide film by the gate bias. The on/off ratio of the transistor was about 10^2 . The field effect mobility was determined from the output characteristics. As a result, a field effect mobility of about $0.8 \text{ cm}^2(\text{Vs})^{-1}$ was obtained in the saturation region.

[0141] The TFT element formed on a glass substrate showed similar characteristics. The element formed on the PET film was inflected at a radius of curvature of 30 mm, and the same transistor characteristics were measured. No change in transistor characteristics was observed.

[0142] It should be noted that, as described in EXAMPLES above, the film can be used as a channel layer of a TFT by controlling the electron carrier concentration to less than $10^{18} / \text{cm}^3$. The electron carrier concentration is more preferably $10^{17} / \text{cm}^3$ or less and yet more preferably $10^{16} / \text{cm}^3$ or less.

Industrial Applicability

[0143] The amorphous oxide of the present invention can be used in semiconductor devices such as thin film transistors. The thin film transistors can be used as switching elements of LCDs and organic EL displays and are also widely applicable to see-through-type displays, IC cards, ID tags, etc.

Brief Description of the Drawings

[0144]

5 [Fig. 1] Fig. 1 is a graph that shows the relationship between the oxygen partial pressure during the deposition and the electron carrier concentration of an In-Ga-Zn-O amorphous oxide deposited by a pulsed laser deposition method.

10 [Fig. 2] Fig. 2 is a graph that shows the relationship between the electron carrier concentration and electron mobility of an In-Ga-Zn-O amorphous oxide film formed by a pulsed laser deposition method.

15 [Fig. 3] Fig. 3 is a graph that shows the relationship between the oxygen partial pressure during the deposition and the electrical conductivity of an In-Ga-Zn-O amorphous oxide deposited by a high-frequency sputtering method.

20 [Fig. 4] Fig. 4 is a graph showing changes in electron conductivity, electron carrier concentration, and electron mobility of $\text{InGaO}_3(\text{Zn}_{1-x}\text{Mg}_x\text{O})_4$ deposited by pulsed laser deposition against x.

[Fig. 5] Fig. 5 is a schematic illustration showing a structure of a top gate TFT element.

25 [Fig. 6] Fig. 6 is a graph showing a current-voltage characteristic of a top gate TFT element.

[Fig. 7] Fig. 7 is a schematic illustration showing a pulsed layer deposition device.

30 [Fig. 8] Fig. 8 is a schematic illustration showing a sputter deposition device.

Concluding Remarks

35 **[0145]** In view of the foregoing, the present invention thus discloses in particular the following (1) to (21) below.

(1) An amorphous oxide having an electron carrier concentration less than $10^{18} / \text{cm}^3$.

(2) An amorphous oxide having an electron carrier concentration less than $10^{17} / \text{cm}^3$.

(3) An amorphous oxide having an electron carrier concentration less than $10^{16} / \text{cm}^3$.

(4) An amorphous oxide wherein electron mobility thereof increases with the electron carrier concentration.

(5) The amorphous oxide according to any one of (1) to (4), wherein the electron mobility is more than $0.1 \text{ cm}^2 / (\text{V} \cdot \text{sec})$.

(6) The amorphous oxide according to any one of (1) to (5), exhibiting degenerate conduction.

(7) The amorphous oxide according to any one of (1) to (6), wherein the amorphous oxide is a compound that contains at least one element selected from Zn, In, and Sn as a constituent and is represented by $[(\text{Sn}_{1-x}\text{M}_4)_2\text{O}] \cdot [(\text{In}_{1-y}\text{M}_3)_2\text{O}_3] \cdot b \cdot [(\text{Zn}_{1-z}\text{M}_2)_2\text{O}] \cdot c$ (wherein $0 \leq x \leq 1$, $0 \leq y \leq 1$, $0 \leq z \leq 1$; x, y, and z are not simultaneously 1; $0 \leq a \leq 1$, $0 \leq b \leq 1$, $0 \leq c \leq 1$, and $a + b + c = 1$; M_4 is a group

IV element having an atomic number smaller than that of Sn; M3 is Lu or a group III element having an atomic number smaller than that of In; and M2 is a group II element having an atomic number smaller than that of Zn).

(8) The amorphous oxide according to (7), further comprising at least one element selected from group V elements M5 and W.

(9) The amorphous oxide according to (7), wherein the amorphous oxide is a single compound represented by $(\text{InM3})_2\text{O}_3(\text{Zn}_{1-x}\text{M2}_x\text{O})_m$ (wherein $0 \leq x \leq 1$; m is zero or a natural number less than 6; M3 is Lu or a group III element having an atomic number smaller than that of In; and M2 is a group II element having an atomic number smaller than that of Zn) in a crystallized state or a mixture of the compounds with different values of m.

(10) The amorphous oxide according to (9), wherein M3 is Ga.

(11) The amorphous oxide according to (9) or (10), wherein M2 is Mg.

(12) The amorphous oxide according to any one of (1) to (11), wherein the amorphous oxide is formed on a glass substrate, a metal substrate, a plastic substrate, or a plastic film.

(13) A field effect transistor wherein the amorphous oxide according to any one of (1) to (12) is used in a channel layer.

(14) The field effect transistor according to (13), wherein a gate insulating layer comprises one of Al_2O_3 , Y_2O_3 , and HfO_2 or a mixed crystal compound containing at least two of these compounds.

(15) A thin film transistor comprising a source electrode, a drain electrode, a gate electrode, a gate insulating film and a channel layer, wherein the channel layer comprises an amorphous oxide having an electron carrier concentration of less than $10^{18}/\text{cm}^3$.

(16) The thin film transistor according to (15), wherein the electron carrier concentration of the amorphous oxide is $10^{17}/\text{cm}^3$ or less.

(17) The thin film transistor according to (15), wherein the electron carrier concentration of the amorphous oxide is $10^{16}/\text{cm}^3$ or less.

(18) The thin film transistor according to any one of (15) to (17), wherein the amorphous oxide is an oxide comprising In, Ga, and Zn, and the atomic ratio In:Ga:Zn is 1:1:m ($m < 6$).

(19) The thin film transistor according to any one of (15) to (17), wherein the amorphous oxide is an oxide comprising In, Ga, Zn, and Mg and the atomic ratio In:Ga:Zn:Mg_x is 1:1:m ($m < 6$), wherein $0 < x \leq 1$.

(20) The thin film transistor according to any one of (15) to (17), wherein the amorphous oxide is selected from $\text{In}_x\text{Ga}_{1-x}$ oxides ($0 \leq x \leq 1$), $\text{In}_x\text{Zn}_{1-x}$ oxides ($0.2 \leq x \leq 1$), $\text{In}_x\text{Sn}_{1-x}$ oxides ($0.8 \leq x \leq 1$), and $\text{In}_x(\text{Zn}, \text{Sn})_{1-x}$ oxides ($0.15 \leq x \leq 1$).

(21) The thin film transistor according to any one of (15) to (17) wherein the electron mobility of the amor-

phous oxide increases with the electron carrier concentration.

5 Claims

1. A method of forming a thin film transistor having a gate terminal, a source terminal, and a drain terminal, in which a semiconductor thin film deposited on a substrate is used as a channel layer, **characterized by** depositing the channel layer on the substrate by a vapor-phase growth deposition method using a target of polycrystal of a compound represented by $\text{InGaO}_3(\text{ZnO})_m$, wherein m is a natural number less than 6, wherein the vapor-phase growth deposition is performed to deposit a transparent, semi-insulating and amorphous oxide composed of In, Ga, Zn, and O, in an atmosphere containing oxygen without adding impurity ions to increase electrical resistance and at an oxygen partial pressure so as to adjust an electron carrier concentration to $10^{18}/\text{cm}^3$ or less as measured by Hall-effect measurement and an electron mobility is $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ or more at room temperature.
2. The method of forming a thin film transistor according to claim 1, wherein the substrate is one of a glass plate, a plastic plate or a plastic film.
3. The method of forming a thin film transistor according to claim 1, wherein the vapor-phase growth deposition method is either a pulse laser deposition method or a sputtering method.

Patentansprüche

1. Verfahren zum Bilden eines Dünnschichttransistors mit einem Gate-Terminal, einem Source-Terminal und mit einem Drain-Terminal, wobei eine auf einem Substrat abgeschiedene Halbleiterdünnschicht als eine Kanalschicht verwendet wird, **gekennzeichnet durch** ein Abscheiden der Kanalschicht auf dem Substrat **durch** ein Gasphasenwachstum-Abscheidungsverfahren, bei dem ein Target aus einem Polykristall aus einer Verbindung aus $\text{InGaO}_3(\text{ZnO})_m$ verwendet wird, wobei m eine natürliche Zahl kleiner als 6 ist, wobei die Gasphasenwachstum-Abscheidung durchgeführt wird, um ein aus In, Ga, Zn und O zusammengesetztes transparentes, halbisolierendes und amorphes Oxid in einer Atmosphäre, welche Sauerstoff ohne das Hinzugeben von Verunreinigungs-Ionen enthält, um den elektrischen Widerstand zu erhöhen, und bei einem Sauerstoffpartialdruck abzuscheiden, um die Elektron-Ladungsträ-

gerkonzentration auf $10^{18}/\text{cm}^3$ oder weniger einzustellen, wie **durch** Hall-Effekt-Messung gemessen, und die Beweglichkeit der Elektronen beträgt bei Raumtemperatur $1 \text{ cm}^2/(\text{V}\cdot\text{sec})$ oder mehr.

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2. Verfahren zum Bilden eines Dünnschichttransistors nach Anspruch 1, wobei das Substrat eine Glasplatte, eine Plastikplatte und/oder ein Plastikfilm ist.

3. Verfahren zum Bilden eines Dünnschichttransistors nach Anspruch 1, wobei das Gasphasenwachstums-Abscheidungsverfahren entweder ein gepulstes Laserdepositionsverfahren oder ein Sputterverfahren ist.

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Revendications

1. Procédé de formation d'un transistor en couches minces ayant une borne de grille, une borne de source et une borne de drain, dans lequel une couche mince de semi-conducteur déposée sur un substrat est utilisée comme une couche de canal, **caractérisé par**

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le dépôt de la couche de canal sur le substrat par un procédé de dépôt par croissance en phase vapeur en utilisant une cible d'un polycristal d'un composé représenté par $\text{InGaO}_3(\text{ZnO})_m$, dans laquelle m est un nombre naturel inférieur à 6,

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dans lequel le dépôt par croissance en phase vapeur est exécuté de façon à déposer un oxyde transparent, semi-isolant et amorphe composé d'In, Ga, Zn, et O, dans une atmosphère contenant de l'oxygène sans ajouter d'ions d'impureté pour augmenter une résistance électrique et sous une pression partielle d'oxygène de façon à ajuster une concentration de transporteur d'électrons à $10^{18}/\text{cm}^3$ ou moins telle que mesurée par une mesure par effet Hall et une mobilité d'électrons est $1 \text{ cm}^2/(\text{V}\cdot\text{s})$ ou plus à la température ambiante.

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2. Procédé de formation d'un transistor en couches minces selon la revendication 1, dans lequel le substrat est un parmi une plaque de verre, une plaque de plastique ou un film de plastique.

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3. Procédé de formation d'un transistor en couches minces selon la revendication 1, dans lequel le procédé de dépôt par croissance en phase vapeur est soit un procédé de dépôt par laser pulsé, soit un procédé de pulvérisation cathodique.

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FIG.1

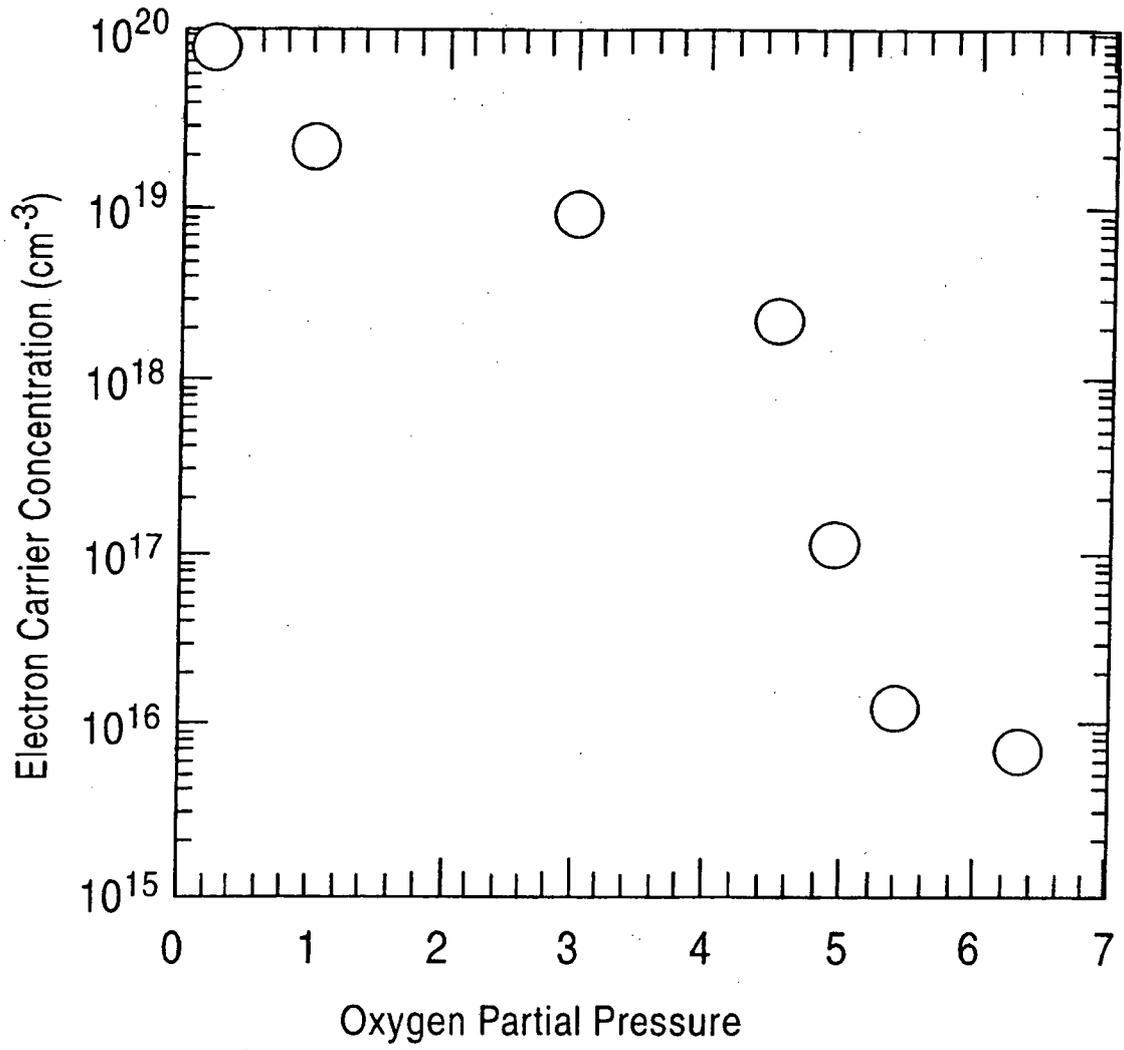


FIG.2

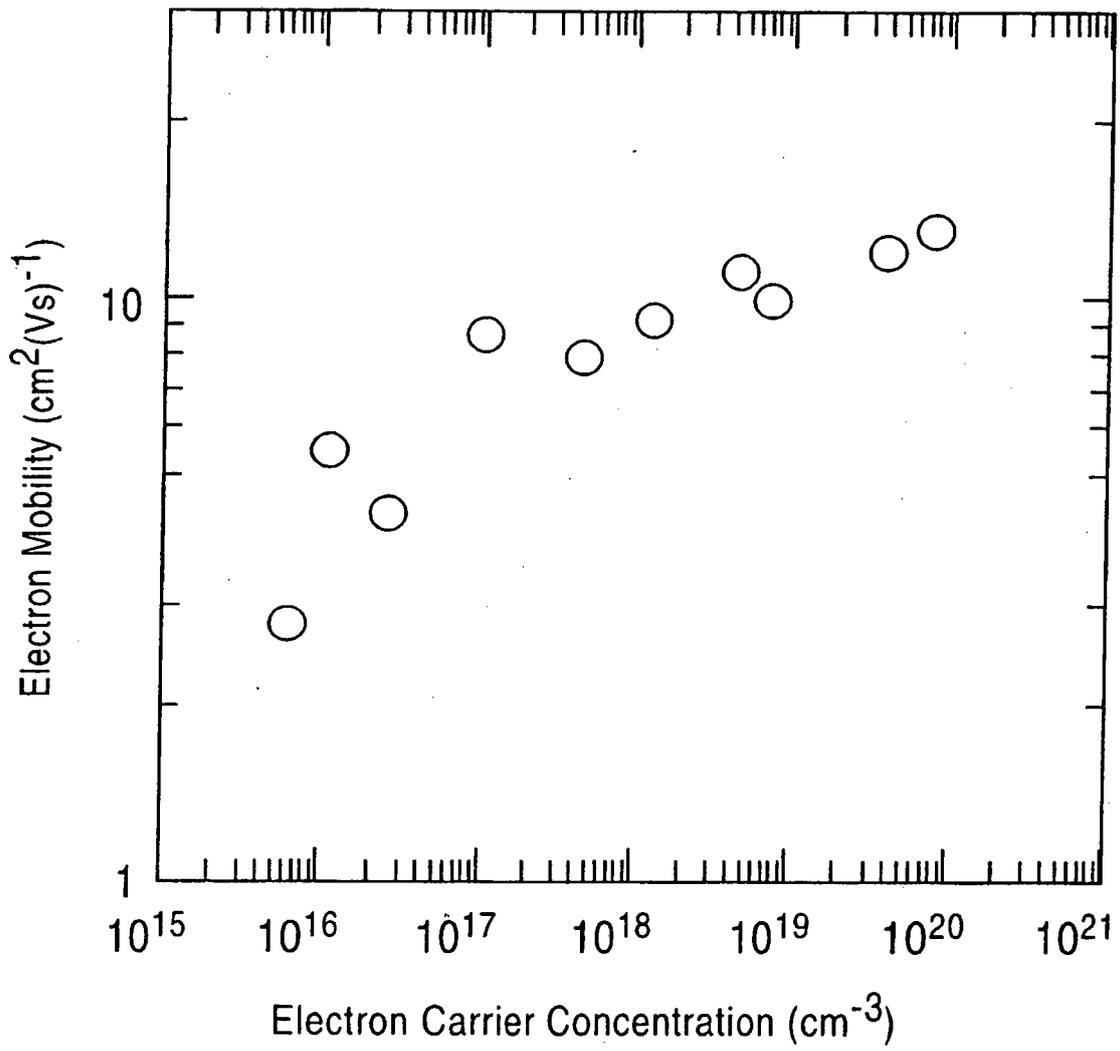


FIG. 3

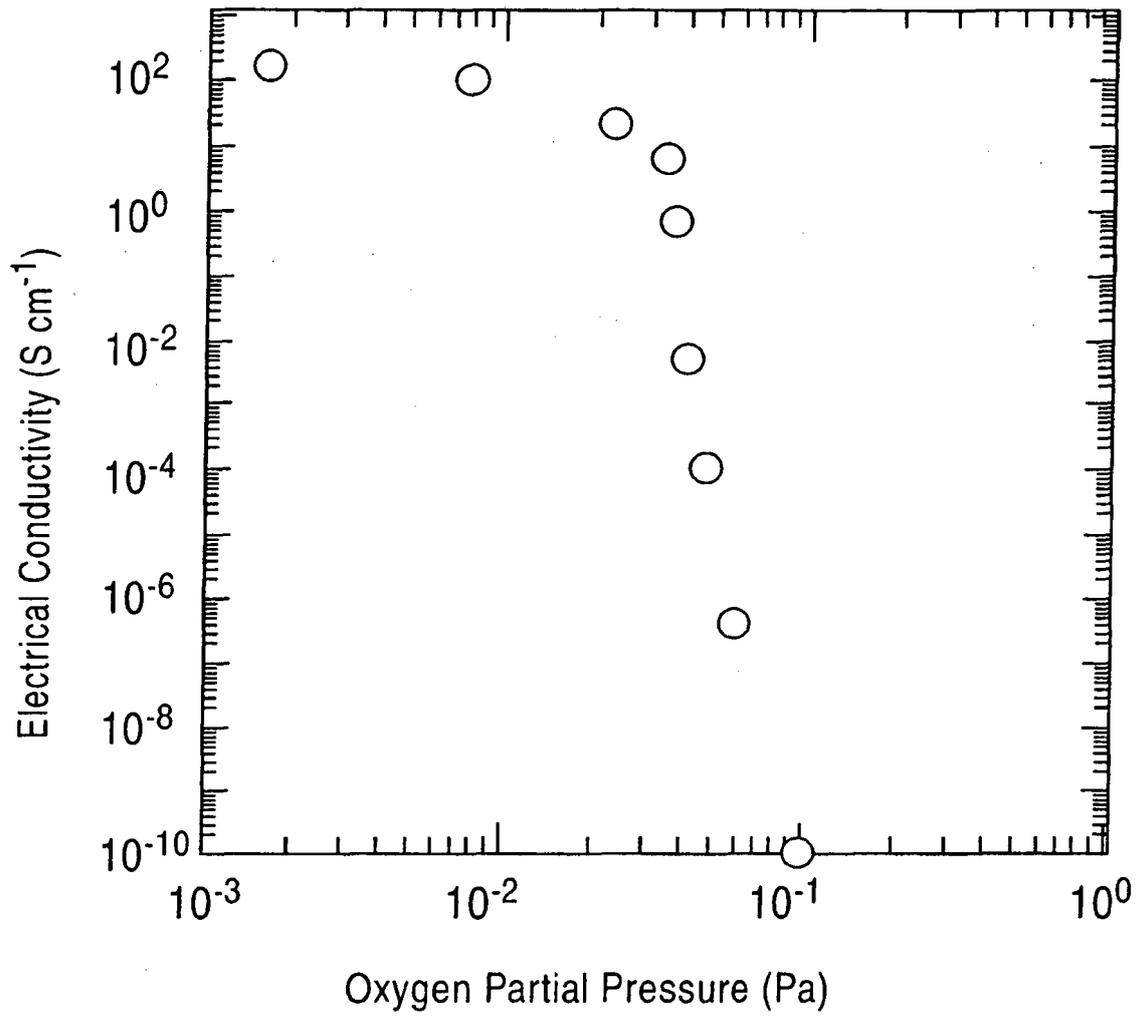


FIG.4A

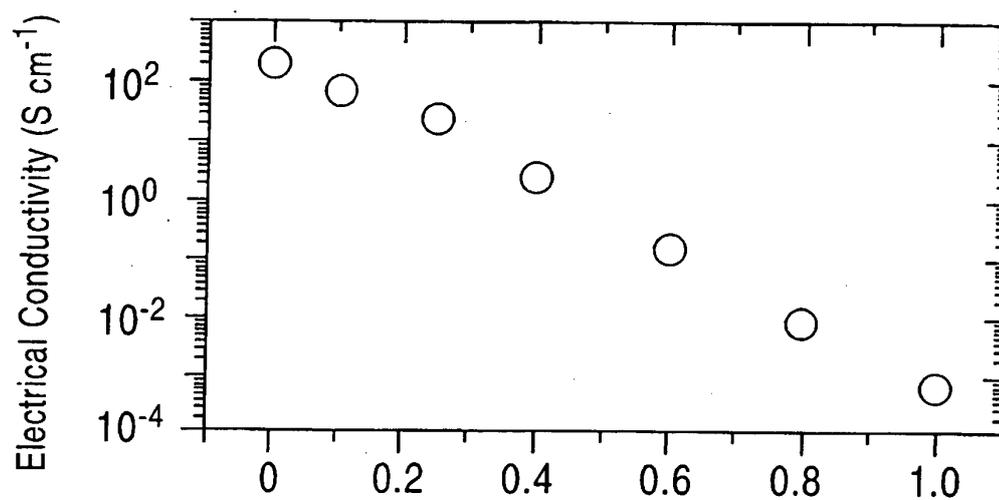


FIG.4B

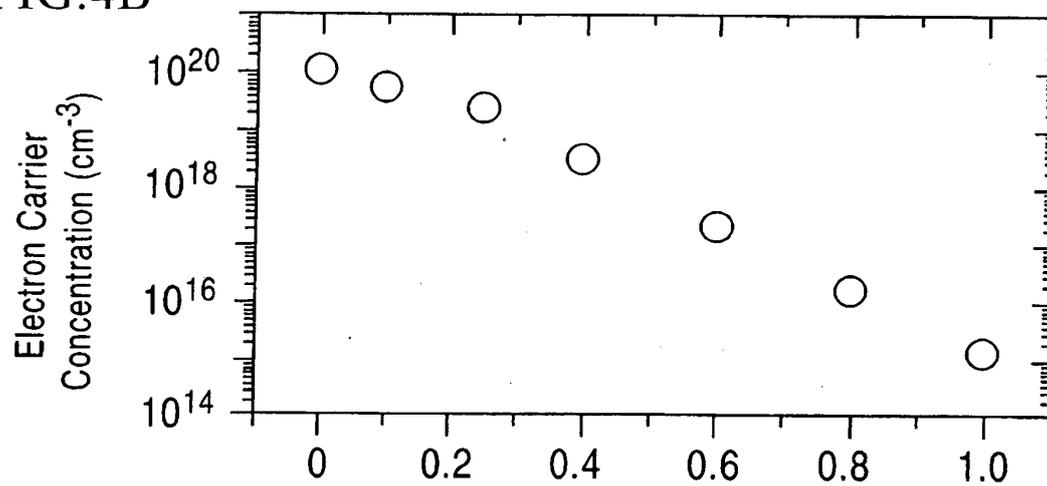


FIG.4C

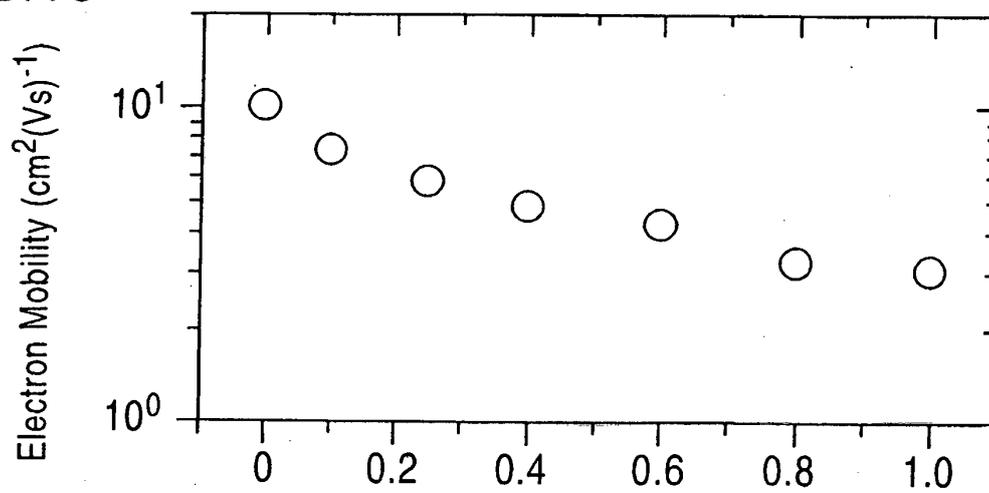


FIG.5

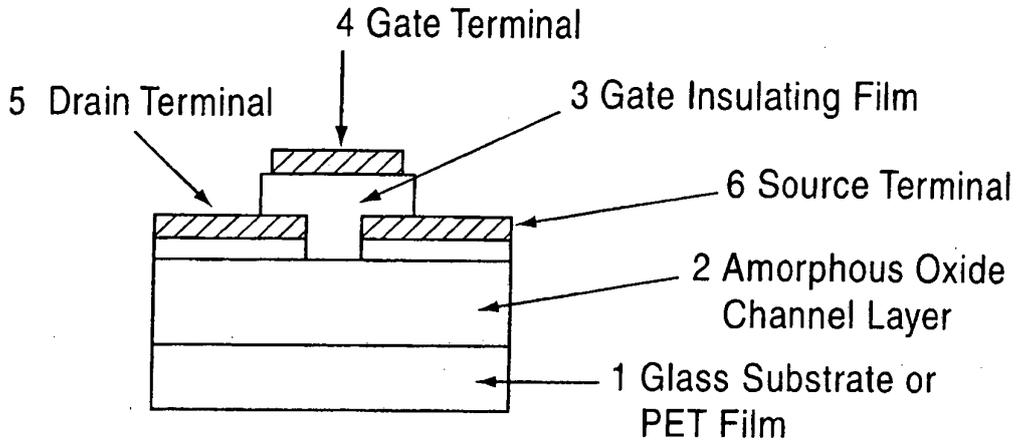
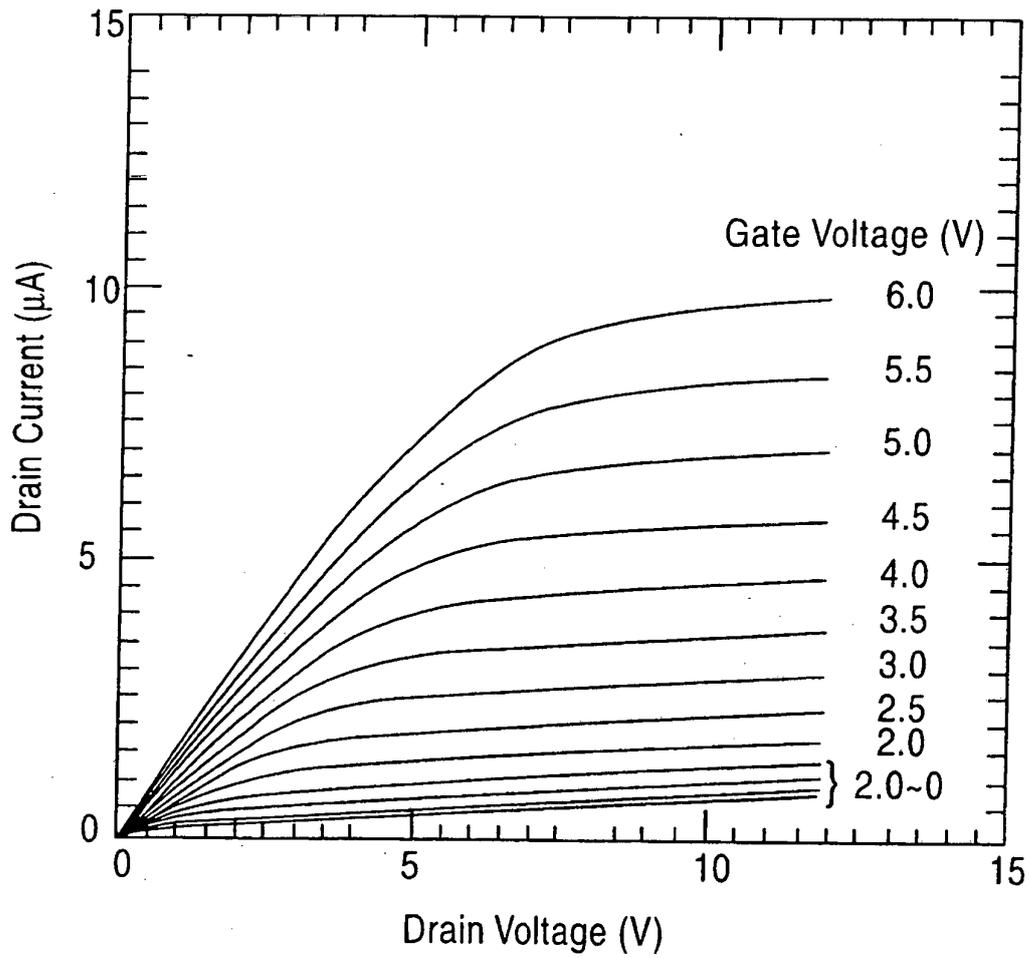


FIG.6



REFERENCES CITED IN THE DESCRIPTION

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